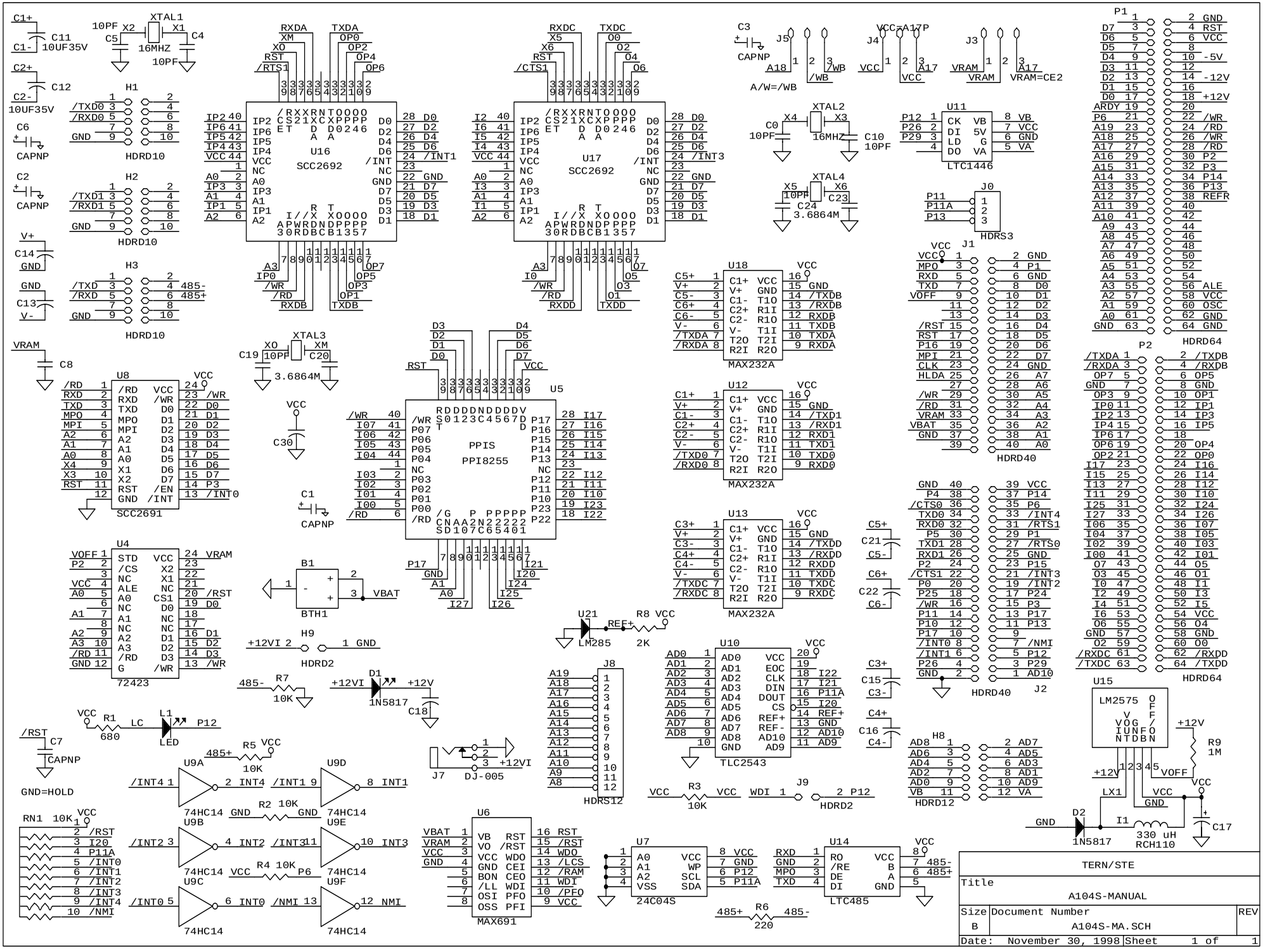
C1+
C11
10UF35V
C1-
C2+
C12
C2-
10UF35V
C6
CAPNP
C2
CAPNP
V+
C14
GND
GND
C13
V-
XTAL1
10PF
X2
X1
C5
16MHZ
C4
10PF
H1
/TXD0
/RXD0
GND
HDRD10
H2
/TXD1
/RXD1
GND
HDRD10
H3
/TXD
/RXD
GND
485-
485+
HDRD10
U16
SCC2692
RXDA
TXDA
/RTS1
IP2
IP6
IP5
IP4
VCC
A0
IP3
A1
IP1
A2
D0
D2
D4
D6
/INT
GND
D7
D5
D3
D1
OP7
OP5
OP3
OP1
/WR
/RD
RXDB
TXDB
U17
SCC2692
RXDC
TXDC
/CTS1
I2
I6
I5
I4
VCC
A0
I3
A1
I1
A2
O0
O2
O4
O6
I0
/WR
/RD
RXDD
TXDD
C3
CAPNP
A18
J5
/WB
A/W=/WB
VCC=A17P
J4
VCC
A17
VCC
J3
VRAM
VRAM
VRAM=CE2
XTAL2
C0
10PF
X4
X3
16MHZ
C10
10PF
U11
LTC1446
P12
P26
P29
VB
VCC
GND
VA
XTAL4
X5
10PF
X6
C23
C24
3.6864M
J0
P11
P11A
P13
HDRS3
P1
D7
D6
D5
D4
D3
D2
D1
D0
ARDY
P6
A19
A18
A17
A16
A15
A14
A13
A12
A11
A10
A9
A8
A7
A6
A5
A4
A3
A2
A1
A0
GND
GND
GND
-5V
-12V
+12V
/WR
/RD
/WR
/RD
P2
P3
P14
P13
REFR
HDRD64
P2
/TXDA
/RXDA
OP7
GND
OP3
IP0
IP2
IP4
IP6
OP6
OP2
I17
I15
I13
I11
I25
I27
I06
I04
I02
I00
O7
O3
I0
I2
I4
I6
O6
GND
O2
/RXDC
/TXDC
/TXDB
/RXDB
OP5
GND
OP1
IP1
IP3
IP5
OP4
I16
I14
I12
I10
I24
I26
I07
I05
I03
I01
O5
O1
I3
I5
I7
O4
GND
O0
/RXDD
/TXDD
HDRD64
J1
VCC
MPO
RXD
TXD
VOFF
/RST
RST
P16
MPI
CLK
HLDA
/WR
/RD
VRAM
VBAT
GND
GND
P1
P4
GND
HDRD40
U18
MAX232A
C5+
V+
C5-
C6+
C6-
V-
/TXDA
/RXDA
C1+
V+
C1-
C2+
C2-
V-
/TXDB
/RXDB
RXDB
TXDB
TXDA
RXDA
U12
MAX232A
C1+
V+
C1-
C2+
C2-
V-
/TXD0
/RXD0
/TXD1
/RXD1
RXD1
TXD1
TXD0
RXD0
U13
MAX232A
C3+
V+
C3-
C4+
C4-
V-
/TXDC
/RXDC
/TXDD
/RXDD
RXDD
TXDD
TXDC
RXDC
C5+
C21
C5-
C6+
C22
C6-
C3+
C15
C3-
C4+
C16
C4-
U8
SCC2691
/RD
RXD
TXD
MPO
MPI
A2
A1
A0
X4
X3
RST
/WR
I07
I06
I05
I04
I03
I02
I01
I00
/RD
/WR
RXD
TXD
MPO
MPI
A2
A1
A0
X2
X1
/EN
/INT
/INT0
C8
VRAM
C30
VCC
C1
CAPNP
XTAL3
XO
XM
C19
10PF
C20
3.6864M
U5
PPI8255
RDDDDNDDDDV
S0123C4567D
T
D
PPIS
PPPPP
CNAA2N22222
SD107C65401
P07
P06
P05
P04
P03
P02
P01
P00
/G
/RD
P17
P16
P15
P14
P13
P12
P11
P10
P23
P22
I17
I16
I15
I14
I13
I12
I11
I10
I23
I22
I21
I20
I24
I25
I26
I27
D3
D2
D1
D0
D4
D5
D6
D7
VCC
RST
U4
72423
VOFF
P2
VCC
A0
A1
A2
A3
/RD
GND
VRAM
/RST
/CS
ALE
CS1
/WR
/RST
D0
D1
D2
D3
B1
BTH1
VBAT
H9
HDRD2
+12VI
GND
U21
LM285
REF+
R8
2K
VCC
U10
TLC2543
AD0
AD1
AD2
AD3
AD4
AD5
AD6
AD7
AD8
GND
EOC
CLK
DIN
DOUT
CS
REF+
REF-
AD10
AD9
I22
I21
P11A
I20
REF+
AD10
AD9
J8
HDRS12
A19
A18
A17
A16
A15
A14
A13
A12
A11
A10
A9
A8
H8
HDRD12
AD8
AD6
AD4
AD2
AD0
VB
AD7
AD5
AD3
AD1
AD9
VA
J2
U15
LM2575
VFOG
IUNTD
OFFBN
+12V
R9
1M
VOFF
LX1
I1
330 uH
RCH110
C17
D2
1N5817
R7
485-
10K
D1
1N5817
+12VI
+12V
C18
R1
680
LC
L1
LED
P12
/RST
C7
CAPNP
GND=HOLD
R5
485+
10K
VCC
J7
DJ-005
+12VI
R3
10K
VCC
VCC
WDI
J9
HDRD2
P12
U9A
U9B
U9C
U9D
U9E
U9F
74HC14
/INT4
/INT2
/INT0
/INT1
/INT3
/NMI
INT4
INT2
INT0
INT1
INT3
NMI
INT1
INT3
NMI
R2 10K
R4 10K
P6
GND
GND
VCC
RN1 10K
/RST
I20
P11A
/INT0
/INT1
/INT2
/INT3
/INT4
/NMI
U6
MAX691
VBAT
VRAM
VCC
GND
VB
VO
VCC
GND
BON
/LL
OSI
OSS
RST
/RST
WDO
/LCS
/RAM
WDI
/PFO
PFO
PFI
U7
24C04S
A0
A1
A2
VSS
VCC
WP
SCL
SDA
P12
P11A
U14
LTC485
RXD
GND
MPO
TXD
RO
/RE
DE
DI
VCC
B
A
GND
485-
485+
R6
220
TERN/STE
Title
A104S-MANUAL
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A104S-MA.SCH
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Date: November 30, 1998
Sheet 1 of 1