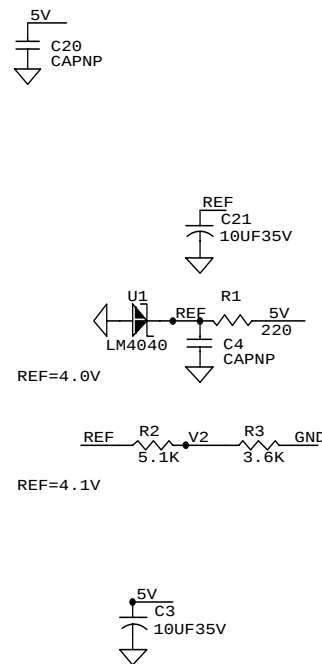
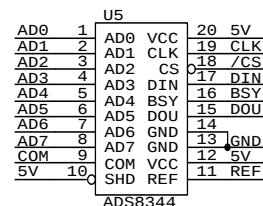
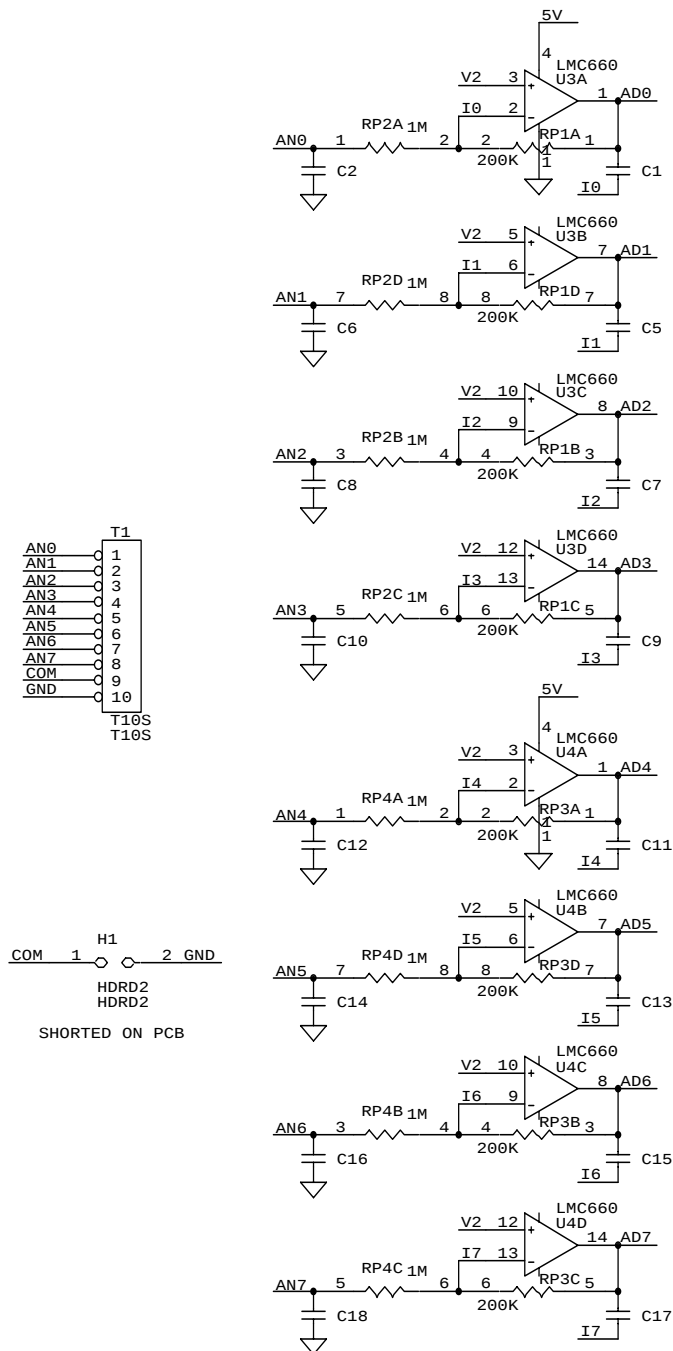


FOR +-10V INPUTS, G=0.2, REF=4.0V, V2=1.65V

FOR +-2V INPUTS, 0-4V OUTPUT, G=1, REF=4.1V, V2=1V, R2/R3=3.1

FOR +-0.25V INPUTS, G=8, REF=4.0V, V2=0.2222V, R2=17K, R3=1K, RP2=100K, RP1=800K

FOR 0-5.2V INPUTS, REF=4.0V, V2=2.26V, R2=0.77K, R3=1K, RP2=1M, RP1=769K

FOR 0-10V INPUTS, G=0.4, REF=4.0V, V2=2.857V, R2=400, R3=1K, RP2=1M, RP1=400K

STE		
Title	SIGNAL CONDITIONING + 16-BIT ADC	
Size	Document Number	REV
B	OPA.SCH	
Date:	March 21, 2006	Sheet 1 of 1