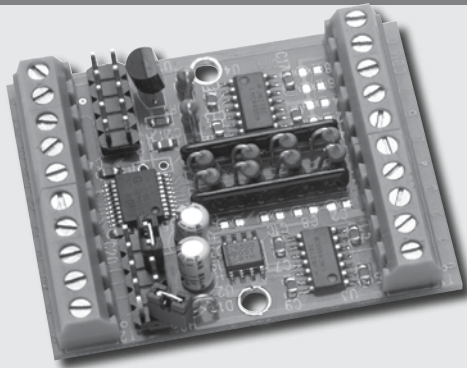


## OPA™ Adding 16-bit ADCs to your embedded system with 3 I/Os



The **OPA** measures 2.0 x 1.6 inches. It can be powered by 5-9V DC. By using OPA, you can add 8 channels of 16-bit ADC(ADS8344, TI) to your embedded system with 3 TTL I/O lines. Multiple OPAs can also be integrated. Sample software programs are available for many TERN controllers. You can also use the OPA as a low cost analog signal conditioning board to convert industrial standard analog input signals from  $\pm 10V$  to 0-4V.

### Order Information

**OPA \$99**

Includes: 8 operational amplifiers for  $\pm 10V$  to 0-4V conversion. Precision 4.096V reference, screw terminals or headers. Not including 16-bit ADCs.

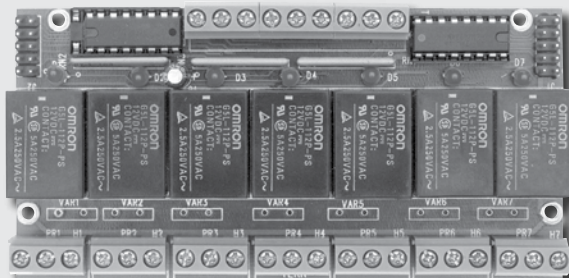
### Add-on Options:

1) 8 ch. 16-bit ADC (ADS8344).....\$30

Order Example: OPA+1=\$99+\$30 = \$129

## Relay7™ (R7)

**7 power relays plus 7 solenoid drivers**



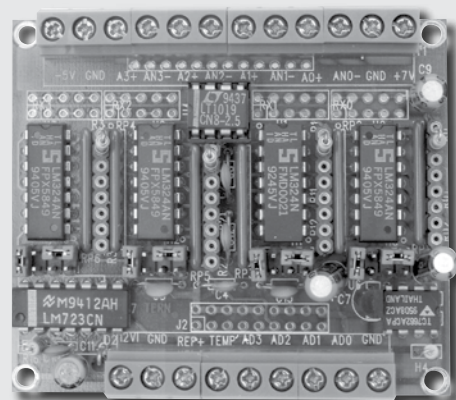
### Features:

- 4.4x2.2x0.8", 4.3 ounces, powered by 12V DC
- 7 power relays (10A, 3 contacts of NO, NC, COM)
- Additional 7 high-voltage I/Os hardware configurable to 0V to 30V inputs, or to 0V to 50V outputs at screw terminal
- Easy to interface to and protect TTL I/O signals.

### Ordering Information

**R7 \$99**

## OPS™ Low Cost Analog Signal Conditioning



**OPS** is designed to convert analog input/output signals between TERN controllers and user applications. It measures 2.8x2.4", and can be powered by 8-12V DC.

**OPS** can be ordered in 4 standard configurations:

**OPS-G2:** 0-4.095V analog inputs and 0-10V analog outputs. Designed for DAC7612/LT1446.

**OPS-G4:** 0-2.5V analog inputs and 0-10V analog outputs. Designed for DAC7625.

**OPS-G05:** 0-10V inputs and 0-5V outputs, for ADCs.

**OPS-G1000:** Adjustable-gain (up to 1000) and high input impedance. It amplifies thermocouple outputs to 0-5V for ADCs.

### Ordering Information

**OPS-xx \$99**

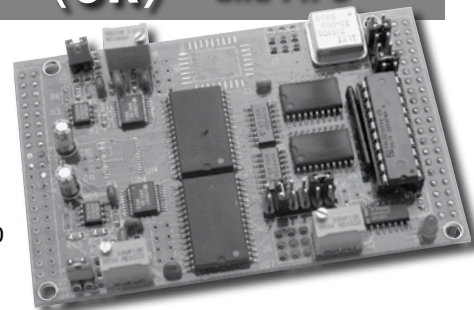
### Add-on Options:

1) LT1014 upgrade for OPS-G1000 (up to 4).....\$10 each

2) Custom gain and offset configuration..... Call

## Grabber™ (GR) 80MHz ADC and FIFO

- 2.3x3.6x0.5 inches
- 80 MHz, 8-bit ADC
- 2 KB 10 ns FIFO for 80MHz ADC data
- 512KB SRAM for up to 20 MHz ADC
- Clock selectable from on-board oscillator, external, or host clock.
- Trigger window based on "one shot"



### Ordering Information – Call



1724 Picasso Avenue, Davis, CA 95618 USA  
Tel: 530-758-0180 • Fax: 530-758-0181

CE FC

sales@tern.com

http://www.tern.com