

Modify the RDC-AMD.pdf file to add:

- A CMP instruction of a memory word (at an odd address) does not set the Trace Flag (TF) correctly if a breakpoint is set at the CMP instruction, when using Paradigm or any other ICE tool.

Do NOT set a breakpoint (or step over) a comparison instruction, if one of the arguments is word-length aligned at an odd address!

Try to compile with word-alignment when possible.